X-998 US
10/082,517

PATENT
Conf. No. 5981

IN THE UNITED STATES PATENT OFFICE

Applicants: Reno L. Sanchez et al.

Assignee: Xilinx, Inc.

Title: Method and System for Inserting Probe Points in
              FPGA-Based System-On-Chip (SoC)

Serial No.:   10/082,517   File Date: 2/22/2002

Examiner: Unknown   Art Unit: 2812

Docket No.:   X-998 US   Conf. No.:   5981

COMMISSIONER FOR PATENTS
Washington, D. C.   20231

PRELIMINARY AMENDMENT

Before taking action in the above-referenced case, please make the following amendments to the claims.

IN THE CLAIMS

Claim 11 should be replaced with:

11.   (Amended)   A machine readable storage having stored thereon, a computer program having a plurality of code sections, said code sections executable by a machine for causing the machine to perform the steps of:

executing software code for a logic analyzer located within an FPGA-based SoC, said software code being the core software code for said logic analyzer utilized for developing and verifying said FPGA-based SoC;

defining at least one monitor probe point within said FPGA-based SoC for analysis by said logic analyzer, wherein the software code and at least one monitor probe point defined are created during customization of said FPGA-based SoC; and

collecting information for said at least one monitor probe point to facilitate analysis of signals while developing and verifying said FPGA-based SoC.